**Weekly Project Report (Week 7)**

1. **Last Week:**

* We finished programming to make the master and slave connect together, then process debugging to test.
* PCB layout was finished and passed all ERC checks.
* Schematics is done

1. **Next Week:**

* Finish debugging master-slave
* Make microphone and speaker work corresponding to microcontroller

1. **Issue:**

* Re-refigured wiring between master and slave board